

**IN THE CLAIMS:**

Please amend claims 1, 6, 15, 18, 20, and 25, and please cancel claims 2-5, 8-14, 16-17, 21-24, 27-30, 32-33, and 35 as set forth below.

1           1.       (Currently Amended) A computer implemented method comprising:  
2     requesting a first deferred procedure call for a first interrupt event associated with a  
3           source;  
4     requesting at least one other different deferred procedure call for a second interrupt event  
5           associated with the source, wherein the first interrupt event comprises one type of  
6           event and the second interrupt event comprises another type of event;  
7     assigning the first deferred procedure call ~~and the at least one other deferred procedure~~  
8           ~~call to a resource~~ to a first thread of a processor supporting a number of threads of  
9           execution;  
10    assigning the second deferred procedure call to a second thread of the number of threads  
11          of execution; and  
12    concurrently processing the first interrupt event ~~with the first deferred procedure call; on~~  
13          the first thread and processing the second interrupt event ~~with the at least one~~  
14          ~~other deferred procedure call~~ on the second thread.

Claims 2-5 (Canceled)

1           6.       (Currently Amended) The method of claim 1, wherein at least one of the  
2 first interrupt event and the second interrupt event comprises a software interrupt further  
3 comprising:  
4 ~~assigning the first deferred procedure call to a resource comprising a first processor;~~  
5 ~~assigning the at least one other deferred procedure call to a resource comprising a second~~  
6 ~~processor; and~~  
7 ~~executing the first deferred procedure call on the first processor while executing the at~~  
8 ~~least one other deferred procedure call on the second processor.~~

1           7.       (Previously Presented) The method of claim 1, further comprising  
2 processing a third interrupt event associated with the source with the first deferred  
3 procedure call, the third interrupt event comprising a third type of event.

Claims 8-14 (Canceled)

1           15.     (Currently Amended) A computer system comprising:  
2     a driver stored in a memory of the computer system, the driver including  
3           an interrupt handler to identify interrupt events associated with a source;  
4           a first deferred procedure call, the first deferred procedure call to process a first  
5                    type of the interrupt events; and  
6           a second different deferred procedure call, the second deferred procedure call to  
7                    process a second type of the interrupt events;  
8           and  
9     a processor supporting a number of threads of execution, the processor to concurrently  
10                    execute the first deferred procedure call on a first thread of the number of  
11                    threads and the second deferred procedure calls call on a second thread of  
12                    the number of threads.

Claims 16-17 (Canceled)

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1           18.     (Currently Amended) The computer system of claim 15, wherein at least  
2     one of the first and second interrupt events comprises a software interrupt the interrupt  
3     ~~handler to assign the first deferred procedure call to the processor and the second deferred~~  
4     ~~procedure call to a second processor for execution.~~

1           19.     (Previously Presented) The computer system of claim 15, wherein the  
2     source comprises a peripheral device coupled with the computer system.

1        20.     (Currently Amended) An article of manufacture comprising:  
2        a machine accessible medium, the machine accessible medium providing instructions  
3        that, when executed by a machine, cause the machine to:  
4               request a first deferred procedure call for a first interrupt event associated with a  
5               source;  
6               request at least one other different deferred procedure call for a second interrupt  
7               event associated with the source, wherein the first interrupt event  
8               comprises one type of event and the second interrupt event comprises  
9               another type of event;  
10              assign the first deferred procedure call ~~and the at least one other deferred~~  
11              ~~procedure call to a resource~~ to a first thread of a processor supporting a  
12              number of threads of execution;  
13              assign the second deferred procedure call to a second thread of the number of  
14              threads of execution; and  
15              concurrently process the first interrupt event ~~with the first deferred procedure call;~~  
16              on the first thread and process the second interrupt event ~~with the at least~~  
17              ~~one other deferred procedure call~~ on the second thread.

Claims 21-24 (Canceled)

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1           25.     (Currently Amended) The article of claim 20, wherein at least one of the  
2 first interrupt event and the second interrupt event comprises a software interrupt the  
3 instructions, when executed, further cause the machine to:  
4 assign the first deferred procedure call to a resource comprising a first processor;  
5 assign the at least one other deferred procedure call to a resource comprising a second  
6 processor; and  
7 execute the first deferred procedure call on the first processor while executing the at least  
8 one other deferred procedure call on the second processor.

1           26.     (Previously Presented) The article of claim 20, wherein the instructions,  
2 when executed, further cause the machine to process a third interrupt event associated  
3 with the source with the first deferred procedure call, the third interrupt event comprising  
4 a third type of event.

Claims 27-30 (Canceled)

1           31.     (Previously Presented) The method of claim 1, wherein the source  
2 comprises a peripheral device of a computer system.

Claims 32-33 (Canceled)

1           34.     (Previously Presented) The article of manufacture of claim 20, wherein  
2     the source comprises a peripheral device of a computer system.

35.     (Canceled)